

6

5

4

3

2

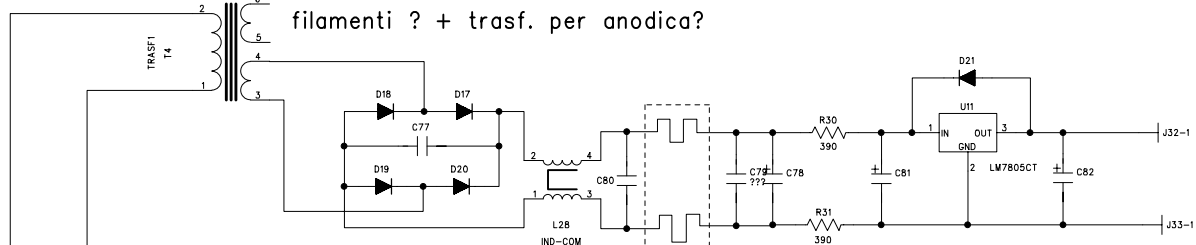
1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

2x12V 30VA

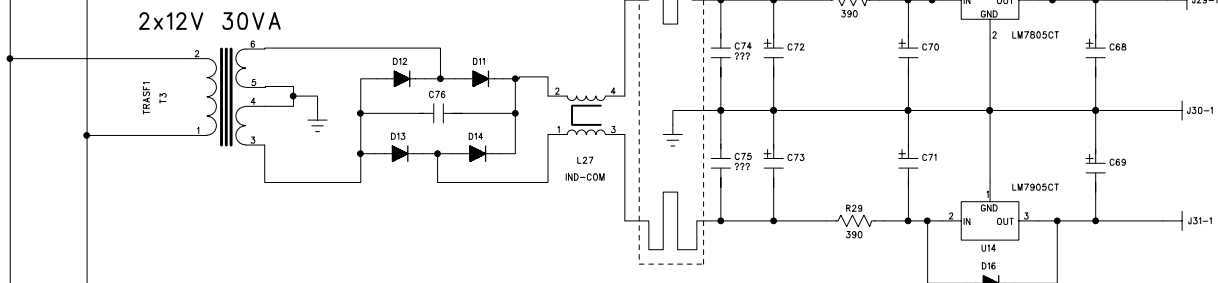
filamenti ? + trasf. per anodica?



VPLL+ (ricevitore - ana) +5V

C

2x12V 30VA

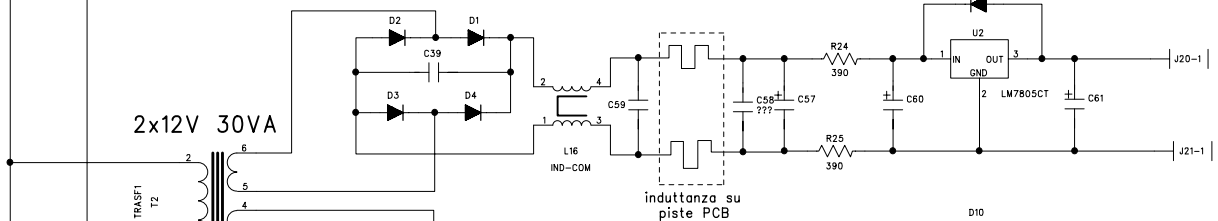


+/- DAC (ana e dig) +/- 5V

B

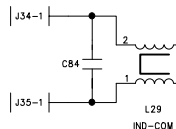
2x12V 30VA

induttanza su piste PCB



VFD+ (filtro digitale) +5V

A



VDD+ (ricevitore-dig) +5V

COMPANY:

TITLE: N-overDAC
alimentatori

DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:	REV:
CHECKED:	DATED:	SCALE:		SHEET:	OF
QUALITY CONTROL:	DATED:				
RELEASED:	DATED:				

D

C

B

A