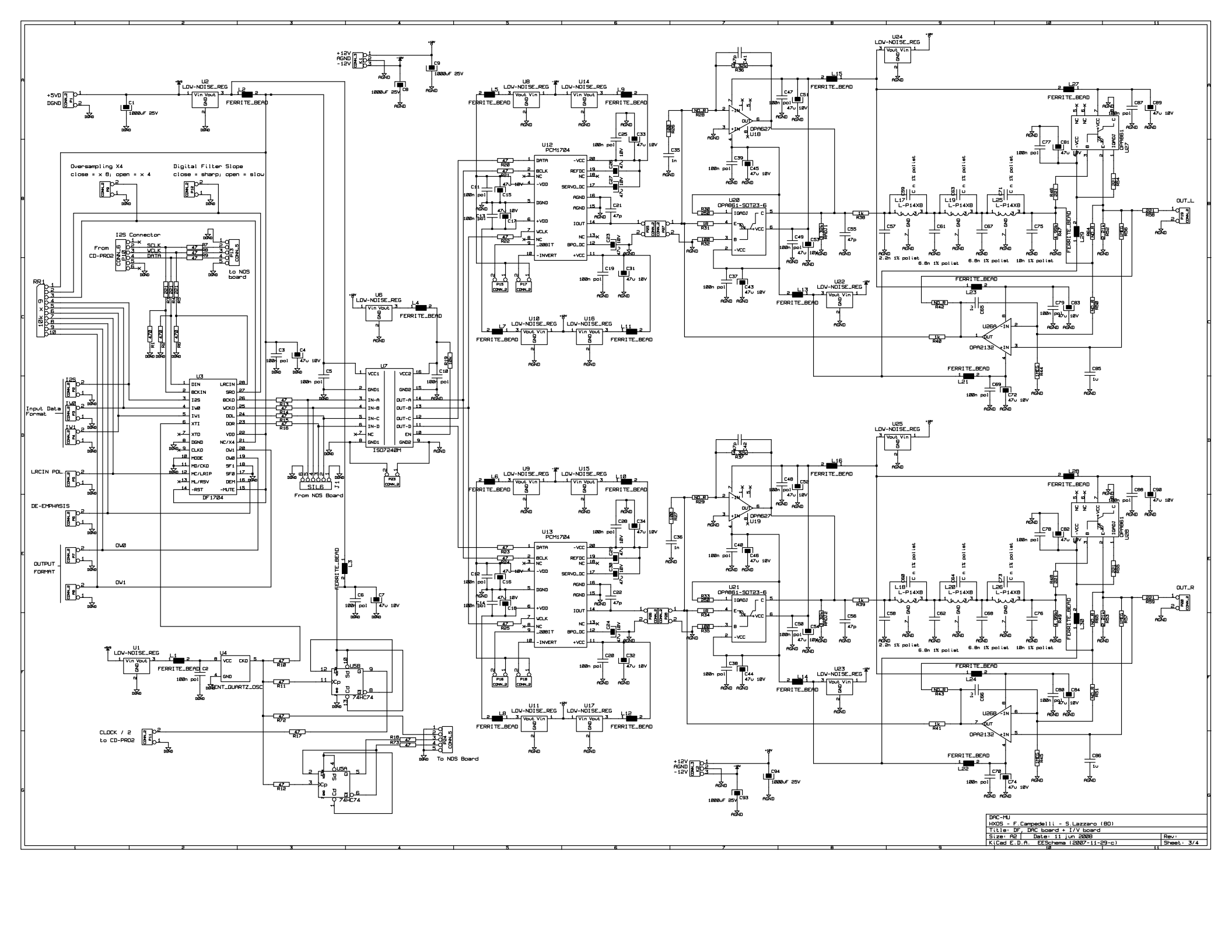
+5VD
DGND
U2
LOW-NOISE_REG
FERRITE_BEAD
Oversampling X4
close = x 8; open = x 4
Digital Filter Slope
close = sharp; open = slow
I2S Connector
From
CD-PRO2
SCLK
WCLK
DATA
to NOS
board
RR1
10k x 9
R1
R2
R3
U3
DIN
BCKIN
I2S
IW0
IW1
XTI
XTO
DGND
CLKO
MODE
MD/CKO
MC/LRIP
ML/RSV
-RST
LRCIN
BCKO
VCKO
DOL
DOR
VDD
NC/X4
OW0
SF1
SF0
DEM
-MUTE
DF1704
Input Data
Format
IW0
IW1
LRCIN POL.
DE-EMPHASIS
OUTPUT
FORMAT
OW0
OW1
U7
ISO7240M
From NOS Board
SIL6
U1
U4
CLOCK / 2
to CD-PRO2
To NOS Board
74HC74
+12V
AGND
-12V
U8
U14
U12
PCM1704
U10
U16
U18
OPA627
U20
OPA861-SOT23-6
U22
OPA2132
U24
U25
U9
U15
U13
U19
U21
U23
L-P14XB
DAC-MU
HXDS - F.Campedelli - S.Lazzaro (BO)
Title: DF, DAC board + I/V board
Size: A2
Date: 11 jun 2008
Rev:
KiCad E.D.A. EESchema (2007-11-29-c)
Sheet: 3/4